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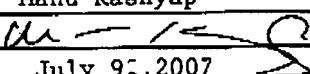
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LSI LOGIC CORPORATION
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Manu Kashyap (Depositor's name)

July 9, 2007 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/713,492	11/14/2003	Douglas J. McKenney	03-0085	9210

TITLE OF INVENTION: FLEXIBLE DESIGN FOR MEMORY USE IN INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	07/10/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
WHITMORE, STACY	2825	716-016000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

LSI Corporation

Milpitas, California USA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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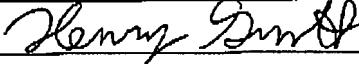
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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Re: 10/713,492
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